IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No PAPERS	
Inventor COPY. C. COPY. C. COPY. C. COPY. C. C. COPY. C.	Dishard Holeshor et al.
Inventor Oklos	nichard noischer et al.
Assignee	Micron Technology, Inc.
Group Art Unit	
Examiner	
Attorney's Docket No	Ml22-1694
Title: Semiconductor Processing Methods	

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

The attached Form PTO-1449 is submitted in compliance with 37 CFR §1.56. No admission is made regarding whether the submitted references are prior art.

This Supplemental Information Disclosure Statement is being filed within three months of the filing of the application or before the mailing of a first Office Action on the merits, whichever occurs last. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. Section 1.17(p) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

Respectfully submitted,

Dated: 1-28-02 Attorney:

D. Brent Kenady Reg. No. 40,045

TC 1700